



Stream Classwork People Grades



PES First Year BCA - Mithun DSouza

A Section

Class code nky746o

Select theme
Upload photo



Share something with your class...



Mithun DSouza
12:58 PM



Digital Fundamentals Study Materials

Follow the below attachments for the exam preparations

Digital fundamentals-U...

Digital fundamentals-U...

Digital fundamentals-U...

digital fundamentals-UN...

Digital fundamentals-U...

Digital fundamentals-U...

K. Sai Latha
Principal 20/5/2020

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.



Add class comment...



Mithun DSouza posted a new assignment: Digital Fundamentals Mock Test Series
May 10



2 class comments

PES First Year BCA - Mithun DSouza
A Section



Stream

Classwork

People

Grades

2 class comments



ವುಷ್ಠ ಕುರಿಂಗ್ ಹೋಂ May 9

Ok sir



Add class comment...



Mithun DSouza

May 9 (Edited May 9)



Dear students...

Digital Fundamentals MCQ Mock test is scheduled at 11AM on 10th May 2020, Sunday

All are strictly informed to attend the same...



Add class comment...



K. Sairathu
20/5/2020
Principal

PES Institute of Advanced Management Studies

NH 206, Sagar Road

SHIVAMOGGA-577 204.

PES First Year BCA - Mithun DSouza
A Section



Instructions Student work




Due May 10, 11:59 AM



Digital Fundamentals Mock Test Series

Mithun DSouza May 10

Test Series No 1

 Digital Fundamentals M...
Google Forms

2 class comments

-  Karan SN May 10
Finished Sir
-  ವುಷ್ ಕುರಿಂಗ್ ಹೋಂ May 10
Done sir...

 Add class comment... 

K. Soudalathra
Principal
PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.



Digital Fundamentals Mock Test

Test Series No 1

* Required



1. Email address *

2. Student Name *

3. 1: In which of the following gates, the output is 1, if and only if at least one input is 1? * 1 point

Mark only one oval.

- A. NOR
- B. AND
- C. OR
- D. NAND

4. 2: The time required for a gate or inverter to change its state is called * 1 point

Mark only one oval.

- A. Rise time
- B. Decay time
- C. Propagation time
- D. Charging time

K. Ganilathu
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

5. 3: What is the minimum number of two-input NAND gates used to perform the function of two input OR gate ? *

1 point

Mark only one oval.

- A. one
 B. two
 C. three
 D. four



6. The Ga*

6. 4: The number of full and half-adders required to add 16-bit numbers is *

1 point

Mark only one oval.

- A. 8 half-adders, 8 full-adders
 B. 1 half-adder, 15 full-adders
 C. 16 half-adders, 0 full-adders
 D. 4 half-adders, 12 full-adders

7. 5: Which of the following gates would output 1 when one input is 1 and other input is 0 ? *

1 point

Mark only one oval.

- A. OR gate
 B. AND gate
 C. NAND gate
 D. both (a) and (c)

K. Sailathra
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

8. 6: The combinational circuit given below is implemented with two NAND gates. To which of the following individual gates is its equivalent? *

1 point



Mark only one oval.

- A. NOT
- B. OR
- C. AND
- D. XOR

9. 7: What is the largest number of data inputs which a data selector with two control inputs can have? *

1 point

Mark only one oval.

- A. 2
- B. 4
- C. 8
- D. 16

10. 8: If a logic gates has four inputs, then total number of possible input combinations is *

1 point

Mark only one oval.

- A. 4
- B. 8
- C. 16
- D. 32

K. Sai Lakshmi
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

5/12/2020

11. 9: A combinational circuit is one in which the output depends on the *

1 point

Mark only one oval.

- A. input combination at the time
- B. input combination and the previous output
- C. input combination at that time and the previous input combination
- D. present output and the previous output



12. 10: The function of a multiplexer is *

1 point

Mark only one oval.

- A. to decode information
- B. to select 1 out of N input data sources and to transmit it to single channel
- C. to transit data on N lines
- D. to perform serial to parallel conversion

13. 11: Which of the following circuit can be used as parallel to serial converter ? 0 points *

Mark only one oval.

- A. Multiplexer
- B. Demultiplexer
- C. Decoder
- D. Digital counter

K. S. Sathish
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

14. 12: The characteristic equation of D flip-flop is: *

1 point

Mark only one oval.

- A. $Q = 1$
- B. $Q = 0$
- C. $Q = \bar{D}$
- D. $Q = D$



15. 13: If four 4 input multiplexers drive a 4 input multiplexer, we get a: *

1 point

Mark only one oval.

- A. 16 input MUX
- B. 8 input MUX
- C. 4 input MUX
- D. 2 input MUX

16. 14: A one-to-four line demultiplexer is to be implemented using a memory. How many bits must each word have? *

1 point

Mark only one oval.

- A. 1 bit
- B. 2 bits
- C. 4 bits
- D. 8 bits

K. Sai Lakshmi
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

17. 15: What is the form of the boolean expressions $AB + B'C' = Y$? *

Mark only one oval.

- A. Product-of-sums
 B. Sum-of-products
 C. Karnaugh map
 D. Matrix



18. 16: A graphical display of the fundamental products in a truth-table is known as *

Mark only one oval.

- A. Mapping
 B. Graphing
 C. T-map
 D. karnaugh-map

19. 17: Flip-flop outputs are always *

1 point

Mark only one oval.

- A. Complimentary
 B. The same
 C. Independent of each other
 D. same as previous input

K. Sankar
20/5/2020

Principal

PES institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.

18: Which of the following flip-flop is free from race-around problem ? *

1 point

Mark only one oval.

- A. Q flip-flop
- B. T flip-flop
- C. SR flip-flop
- D. Master-slave JK flip-flop



21. 19: If the input J is connected through K input of J-K, then flip-flop will behave as a *

1 point

Mark only one oval.

- A. D type flip-flop
- B. T type flip-flop
- C. S-R flip-flop
- D. Master slave JK flip-flop

22. 20: Register is a *

1 point

Mark only one oval.

- A. set of capacitor used to register input instructions in a digital computer
- B. set to paper tapes and cards put in a file
- C. temporary storage unit within the CPU having dedicated or general purpose use
- D. part of the main memory

This content is neither created nor endorsed by Google.

Google Forms

K. Sankar
20/5/2020
Principal

PMS Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.



Instructions

Student work



Digital Fundamentals Mock Test Series

43

Turned in

12

Assigned

Digital Fundamentals Mock Test
Google Forms

All



Abdul Razak

Turned in late



Abhinam Boss

Turned in



Adithya DG

Turned in



Akshatha M

Turned in



Anagha
Bhoopalam T R

Turned in



Anju P shetty

Turned in



Anvitha S

Turned in



B SANDHYA

Turned in



Bhoomika TS

Turned in



Chandu Arya

Turned in



Divya Cb

Turned in



ESHAN ULLA
KHAN

Turned in late



Karan SN

Turned in late



Kavya Gs

Turned in



Loyal Dsouza

Turned in

K. Sankar
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.



Madhura kanasu



Maithili Bhat



Manasa KC


5/12/2020

≡ PES First Year BCA - Mithun DSouza
A Section


Instructions Student work





 Mohammed Faiz
Turned in late


 MOHAMMED
SABEEL
Turned in


 Neha Kousar
Turned in

 Padmanabha
Shanbhag
Turned in late

 Pooja Gowda
Turned in


 Poojitha.s
Poojitha.s
Turned in

 Preethi Pawaar
Turned in


 Preethi R
Turned in


 Raghuram A.P
Turned in


 Sadiya fathima
Turned in

 Safrin Kazi
Turned in

 Salma Sonu
Turned in late


 Sameer Sulthan
Turned in late

 Shamanth S
Turned in late


 Sharlin
Lawrence
Turned in

 Sj salim
Turned in late

 Sneha Deepu
Turned in

 SQUAD GAMERS
Turned in late

 Uzra Arfaath
Turned in


 VARSHA C H
Turned in

 Yashaswini
Gowda
Turned in late


K. Sankar
20/12/20
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road

SHIVAMOGGA-577 204.

 ?
ವುಷ್ಕ ಕುರಿಂಗ್ ಹೋಲ

 Akshatha
Acharya

 Chethan chethu



Instructions

Student work



Jayanth_soul
Missing

Khan Samiya
Missing

Kiran Thorath
Missing

madhura hs
Missing

Manasa Mansi
Missing

Sameer
Musaveer
Missing

Vyshak Hr
Missing

K. S. Sathya
20/5/2020
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.



1:16 HD 4G



PES First Year BCA - Mithun D A Section

Share with your class...

Mithun DSouza
12:58 PM

Digital Fundamentals Study Materials

Follow the below attachments for the exam preparations

Add class comment

New assignment: Digital Fundamentals Mock Test Series
May 10

2 class comments

Stream Classwork People



1:17 HD 4G



PES Second Year BCA- Mithun D... A Section

Share with your class...

Mithun DSouza
1:07 PM

Classroom notes materials...

Follow the below attachments for exam preparations...

Add class comment

Mithun DSouza
1:04 PM

Unit 5 - Windowing and Clipping

Stream Classwork



K. Sankar
Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204

[Signature]
20/5/2020

Principal

PES Institute of Advanced Management Studies
NH 206, Sagar Road
SHIVAMOGGA-577 204.