

PES First Year-BCA - Mithun DSouza



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Stream Classwork

People

Grades



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A Section

Class code nky7460

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Mithun DSouza

12:58 PM

Digital Fundamentals Study Materials

Follow the below attachments for the exam preparations

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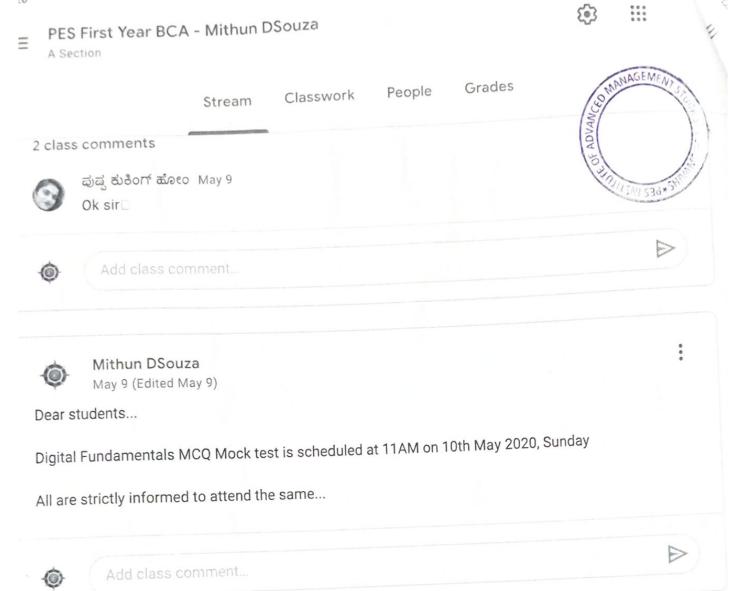
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Mithun DSouza posted a new assignment: Digital Fundamentals Mock Test Series May 10

2 class comments



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A Section

Instructions

Student work







Due May 10, 11:59 AM

Digital Fundamentals Mock Test Series

Mithun DSouza May 10

Test Series No 1



Digital Fundamentals M...

Google Forms

2 class comments



Karan SN May 10

Finished Sir



ಪುಷ್ಪ ಕುಕಿಂಗ್ ಹೋಂ May 10

Done sir...



Add class comment...

D

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Digital Fundamentals Mock Test

Test Series No 1 * Required



Email address *	
Student Name *	
	1 point
1: In which of the following gates, the outinput is 1? *	utput is 1, if and only if at least one
Mark only one oval.	
A. NOR	
B. AND	
C. OR	
D. NAND	
	a hango its state is called * 1 point
2: The time required for a gate or invert	ter to change its state is easily
Mark only one oval.	
A. Rise time	
B. Decay time	Principal PES Institute of Advanced Management Studi
C. Propagation time	K- Saint no 15/1
D. Charging time	Principal
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72020	NAND gates used to perform	1 point
5		
	the function of two input OR gate :	EWENT STOOL
	Mark only one oval.	J. B.
	Mark only one oval.	18
	A. one	ZIII SNIS?
	B. two	Samuel and Samuel
	C. three	
	D. four	
6.	4: The number of full and half-adders required to add 16-bit numbers is *	1 point
	Mark only one oval.	
	A. 8 half-adders, 8 full-adders	
	B. 1 half-adder, 15 full-adders	
	C. 16 half-adders, 0 full-adders	
	D. 4 half-adders, 12 full-adders	
7.	5: Which of the following gates would output 1 when one input is 1 and other input is 0 ? *	1 point
	Mark only one oval.	
	A. OR gate	
	B. AND gate	
	C. NAND gate	
	D. both (a) and (c)	
		Man

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6: The combinational circuit given below is implemented with two NAND gates. To which of the following individual gates is its equivalent? *



1 point



Mark only one oval.

- A. NOT
- B. OR
- C. AND
- D. XOR
- 9. 7: What is the largest number of data inputs which a data selector with two control inputs can have ? *

1 point

Mark only one oval.

- A. 2
- B. 4
- C. 8
- D. 16
- 10. 8: If a logic gates has four inputs, then total number of possible input combinations is *

1 point

Mark only one oval.

- () A. 4
- B. 8
- C. 16
- D. 32

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9: A combinational circuit is one in which the output depends on the * 11. Mark only one oval. A. input combination at the time B. input combination and the previous output C. input combination at that time and the previous input combination D. present output and the previous output 1 point 10: The function of a multiplexer is * 12. Mark only one oval. A. to decode information B. to select 1 out of N input data sources and to transmit it to single channel C. to transit data on N lines D. to perform serial to parallel conversion 11: Which of the following circuit can be used as parallel to serial converter? O points Mark only one oval. A. Multiplexer B. Demultiplexer C. Decoder D. Digital counter

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A	12: The characteristic equation of D flip-flop is: *
	Mark only one oval.
	\bigcirc B. Q = 0
	$\bigcirc C. Q = D$
	D. Q= D
15.	13: If four 4 input multiplexers drive a 4 input multiplexer, we get a: * 1 point
	Mark only one oval.
	A.16 input MUX
	B. 8 input MUX
	C. 4 input MUX
	D. 2 input MUX
16.	14: A one-to-four line demultiplexer is to be implemented using a memory. 1 point How many bits must each word have ? *
	Mark only one oval.
	A. 1 bit
	B. 2 bits
	C. 4 bits
	D. 8 bits
	K. Smilather

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		Digital F	undamenta	V2 *		
5/12/2020		I-on expr	essions AB+	B,C, = 4 \		1 ties
1.7	. 15: What is the form of the boo	leanexp			Wallet.	SEMENT O
	Mark only one oval.				NDVANCED	
	A. Product-of-sums				121	
	B. Sum-of-products				1	UTIT SIN 235
	C. Karnaugh map					
	D. Matrix					
					la is know!	1 point
18.	16: A graphical display of the fun	ndamenta	I products ir	a truth-tabl	E 13 K110	***************************************
10.	as *					
	Mark and an and					
	Mark only one oval.					
	A. Mapping		*			
	B. Graphing					
	C. T-map					
	D. karnaugh-map					
19. 17	7: Flip-flop outputs are always *					1 point
M	ark only one oval.					
	A. Complimentary					
	B. The same					
	C. Independent of each other					
	D. same as previous input					

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	diodinentals Mook Test	
60	18: Which of the following flip-flop is free from race-around problem ? *	1 point
	Mark only one oval.	100 st. 2 412 111 5 1015
	A. Q flip-flop	(O)
	B. T flip-flop	(3)
	C. SR flip-flop	and the same of th
	D. Master-slave JK flip-flop	
21.	19: If the input J is connected through K input of J-K, then flip-flop will behave as a *	1 point
	Mark only one oval.	
	A. D type flip-flop	
	B. T type flip-flop	
	C. S-R flip-flop	
	D. Master slave JK flip-flop	
22.	20: Register is a *	1 point
	Mark only one oval.	
	A. set of capacitor used to register input instructions in a digital computer	
	B, set to paper tapes and cards put in a file	
	C. temporary storage unit within the CPU having dedicated or general purpo	se use
	D. part of the main memory	

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A Section

Instructions Student work



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43

12

Turned in

Assigned



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All





Abdul Razak



Abhinam Boss



Adithya DG

Turned in late

Turned in

Turned in



Turned in

Akshatha M



Anagha

Bhoopalam T R

Anju P shetty

Turned in

Turned in



Anvitha S



B SANDHYA



Bhoomika TS

Turned in

Turned in

Turned in



Chandu Arya



Divya Cb



ESHAN ULLA

Turned in

Turned in

Turned in late

KHAN



Karan SN



Kavya Gs



Loyal Dsouza

Turned in late

Turned in



Principal





Maithili Bhat

https://classroom.google.com/c/MTI0MzE3NDk1MTIw/a/MTAwMzc1ODg0NjAz/submissions/by-status/and-sort-name/all



Manasa KC

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1/3



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Student work





Mohammed Faiz

Turned in late



MOHAMMED SABEEL

Turned in



Neha Kousar

Turned in



Padmanabha Shanbhag

Turned in late



Pooja Gowda

Turned in



Poojitha.s Poojitha.s

Turned in



Preethi Pawaar

Turned in



Preethi R

Turned in



Raghuram A.P

Turned in



Sadiya fathima

Turned in



Safrin Kazi

Turned in



Salma Sonu

Turned in late



Sameer Sulthan

Turned in late



Shamanth S

Turned in late



Sharlin Lawrence

Turned in



Sj salim

Sneha Deepu

Turned in



SQUAD GAMERS

Turned in late



Turned in

Uzra Arfaath



VARSHA C H

Turned in



Yashaswini

Gowda

Turned in late

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Akshatha Acharya



Chethan chethus HIVAMOGGA-577 204.

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Jayanth _soul

Missing



Khan Samiya

Missing



Kiran Thorath

Missing



madhura hs

Missing



Manasa Mansi

Missing



Sameer Musaveer

Missing



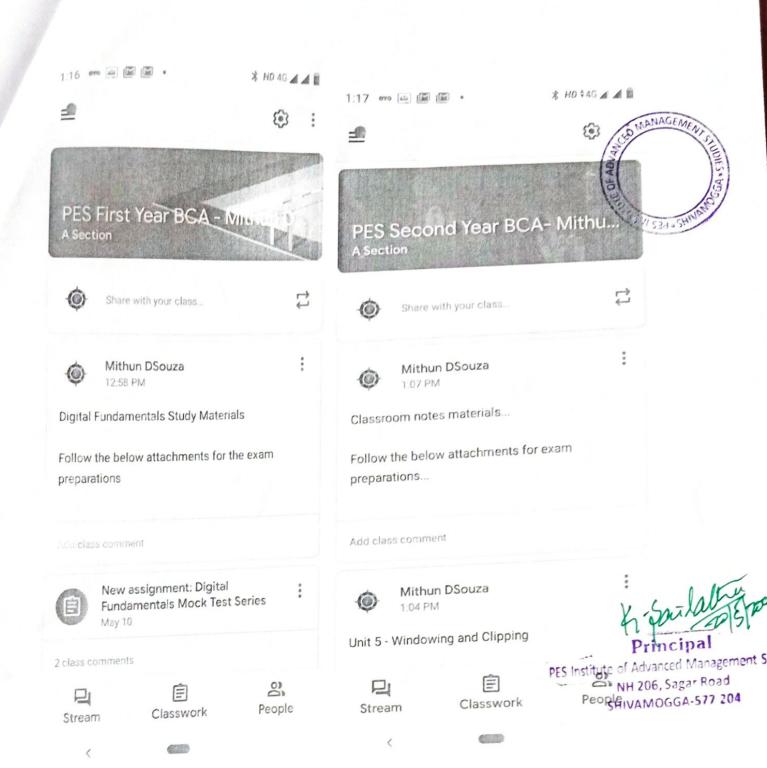
Vyshak Hr

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