



PESITRONICS

Newsletter

Volume 9, Issue 2

Department of Electronics & Communication Engineering

VISION

To be the most preferred institution for engineering & management education, research & entrepreneurship by creating professionally superior & ethically strong global manpower.

MISSION

- Provide high-quality engineering and management education with technical excellence for global requirements.
- Promote research and entrepreneurship with collaboration, modern facilities, and strong mentorship.
- Build ethically grounded graduates who succeed globally, lead with integrity and commit to lifelong learning and social responsibility.

Program Educational Objectives (PEO)

PEO1: To develop the ability among students to understand the concept of core subjects.

PEO2: To give exposure to emerging technologies, adequate training and opportunities to work as a team on multidisciplinary projects with effective communication skills.

PEO3: To cultivate ethical practices in Professional, Societal Environmental needs by engaging in life-long learning.

Program Specific Outcomes (PSOs)

PSO1: Analyse and design analog & digital circuits or systems for a given specification and function.

PSO2: Implement functional blocks of hardware-software co-designs for signal processing and communication applications.

STUDENTS ACTIVITIES/ACHIEVEMENTS

SL No.	Name of the Student	Event / Certification Course / Training Participated (Name, Organizer)	Date	Participation / Position
1	Yogesh C L	Data Analyst ,One Roadmap Skill Certification Test	17/06/2026	Course Completion
2	Hemanth B	SQL Project Bootcamp, Solve Real Business Problems,SkillEcted	17/05/2026	Course Completion
3	Sangeetha K Devanga	"CIRCUIT DRIFT 26"conducted by department of CSE(AIML) & Forge Nexus, VVCE Mysuru, Vidyavardhaka College of Engineering.	15/05/2026	Participation
4	Brunda C Gowda	Digital Image Processing & Great Learning	14/05/2026	Course Completion
5	Sahana H P, Harshitha N S Saqib Ulla, Hemanth B, Rajeev Hegde, Akash K Mariguddi	ASTRIX 2026- THE BUILDVERSE, two day innovation challenge showcasing creativity, problem solving and technological excellence, CSE(DS), PESITM Shivamogga	2/5/2026 & 3/05/2026	Participation
6	Rajeev Hegde, H S Sandeep, HP Sahana, Prashanth G, Sumaiya, Swathi N B, Rahul D J, Hemeanth B, Lakshmi K, Manoj S R, Harshitha N S, Indresh H R, Likhith R S, Mamatha, Varalaxmi B Hiremath, Nihal P Mutgi, Pushyami C R	CODEBEGIN 2026 Hackathon "organised by the Department of Computer Science & Engineering at PES Institute of Technology & Management in Shimoga in collaboration with GeeksforGeeks	29/04/2026	Participated

7	Sowjanya B	Digital Image Processing & Great Learning	29/04/2026	Course Completion
8	Nikhil P G	Hack Malenadu Hackathon, PESITM, Shivamogga	17/04/2026	Participated
9	Nihal Mutgi	Introduction to Data Science, Spring Board, Infosys.	1/4/2026	Course Completion
10	Rakshitha M K	Full Stack Development Internship Program, SkyForger technologies	20/03/2026 to 20/04/2026	Internship Completion

Nasscom -IT-ITeS SSC

11	Vinay N S, Sagar S H, Rakshitha MK, Jayanth K S, Nikhil P S, Raghu, Shashank Sathish, Vinayak Hadapad, Sihas Ghodke, Yuvaraj S, Yashawanth B R, Abhishek Nidoni, Mannojkumar G C, ShankarGouda Patil, Arpitha N, Apoorva H S	Acquiring Data, Aligned to Competency Standards Developed by IT-ITeS Sector Skills Council Nasscom in collaboration with Industry and Government	15/03/2026	Course Completion
12	Druthi G K, Shivayogi G Adarakatti, Bhargavi, Manjushree Nagalingappa Halawagal	Acquiring Data, Aligned to Competency Standards Developed by IT-ITeS Sector Skills Council 49Nasscom in collaboration with Industry and Government	15/03/2026	Course Completion
13	Sachin H S	HACKATHON, Hack For Hire 2026, organised by Anveshana TBI, Shivamogga	13/03/2026 to 14/03/2026	Participation
14	Nishitha S, Shailaja K J	Hackfest-1.0 a National Level Hackathon, organised by Byte Brigade Club at Jawaharlal Nehru National College of Engg, Shivamogga	13/03/2026 to 14/03/2026	Participation

15	Sushmitha Suresh Hegde	Machine Learning Intern with Cognifyz Technologies	03/02/2026 to 05/03/2026	Participation
16	Priyanka M K	YUKTI-2K26- Collage Making,Regional Level Techno-VTU Regional Centre, Mysuru	17/04/2026	Participated
17	Vanitha S S, Nesara H C, Shreya B, Shailaja K J, Sahana Rampur	YUKTI-2K26- Collage Making,Regional Level Techno-VTU Regional Centre, Mysuru	19/02/2026 to 20/06/2026	Participated
18	Hemanth B	Internship Common Aptitude Test , iCAT	30/01/2026	Participated
19	Anusha Bidarakatti	Digital Marketing Fundamentals, IIDE	30/01/2026	Participated

NPTEL Course

1	Chirag D S	Programming in JAVA	January - April 2026	82%
2	Tharun K S	Programming in JAVA	January - April 2026	82%
3	Rakshitha M K	Programming in JAVA	January - April 2026	76%
4	Shailaja K J	Programming in JAVA	January - April 2026	55%
5	Akash K Mariguddi	The Joy of Computing Using Python	January - April 2026	60%
6	Hemanth B	The Joy of Computing Using Python	January - April 2026	51%
7	Gowrishree M	The Joy of Computing Using Python	January - April 2026	52%
8	Sahana K	Cloud Computing	January - April 2026	71%
9	Sabah Suhana	Introduction to Internet of Things	January - April 2026	79%
10	Harshitha N S	Digital Design using Verilog	January - April 2026	54%
11	Neha K Hurali	Introduction to Embedded Systems	January - April 2026	54%

SL No.	Name of the Student	Event / Certification Course / Training Participated (Name, Organizer)	Date	Participation / Position
1	Shreya Sawarn	Cadence RTL to GDSII Flow v7.0	11/5/2026	Course Completion
2	Shreyash B V	Cadence RTL to GDSII Flow v7.1	11/5/2026	Course Completion
3	Shubham Kumar	Cadence RTL to GDSII Flow v7.2	11/5/2026	Course Completion
4	Brunda C Gowda	Cadence RTL to GDSII Flow v7.3	4/5/2026	Course Completion
5	Dhanuja V	Cadence RTL to GDSII Flow v7.4	4/5/2026	Course Completion
6	Aishwarya D	Cadence RTL to GDSII Flow v7.5	3/5/2026	Course Completion
7	Shivani Sureban	Cadence RTL to GDSII Flow v7.6	3/5/2026	Course Completion
8	Jeevan K V	Cadence RTL to GDSII Flow v7.7	3/5/2026	Course Completion
9	Triveni J	Cadence RTL to GDSII Flow v7.8	3/5/2026	Course Completion
10	Madhu Patil	Cadence RTL to GDSII Flow v7.9	3/5/2026	Course Completion
11	Keerthana K G	Cadence RTL to GDSII Flow v7.10	3/5/2026	Course Completion
12	Kavya Suresh B	Cadence RTL to GDSII Flow v7.11	2/5/2026	Course Completion
13	Umesh Lamani	Cadence RTL to GDSII Flow v7.12	2/5/2026	Course Completion
14	Revanth D K	Cadence RTL to GDSII Flow v7.13	2/5/2026	Course Completion
15	N K Gowtham	Cadence RTL to GDSII Flow v7.14	2/5/2026	Course Completion
16	Keerthana B T	Cadence RTL to GDSII Flow v7.15	2/5/2026	Course Completion
17	Vinuth V E	Cadence RTL to GDSII Flow v7.16	2/5/2026	Course Completion
18	Vikas N	Cadence RTL to GDSII Flow v7.17	2/5/2026	Course Completion
19	S H Swathi	Cadence RTL to GDSII Flow v7.18	2/5/2026	Course Completion
20	Srujana B S	Cadence RTL to GDSII Flow v7.19	2/5/2026	Course Completion
21	Shubhashree J K	Cadence RTL to GDSII Flow v7.20	2/5/2026	Course Completion
22	Shreeshail M H	Cadence RTL to GDSII Flow v7.21	2/5/2026	Course Completion
23	Shivaraj Bhovi	Cadence RTL to GDSII Flow v7.22	2/5/2026	Course Completion
24	Shivakanta Gowda J K	Cadence RTL to GDSII Flow v7.23	2/5/2026	Course Completion
25	Shashank N S	Cadence RTL to GDSII Flow v7.24	2/5/2026	Course Completion
26	Sachin H S	Cadence RTL to GDSII Flow v7.25	2/5/2026	Course Completion
27	Sanjayya P S	Cadence RTL to GDSII Flow v7.26	2/5/2026	Course Completion
28	Rishi N Sirsate	Cadence RTL to GDSII Flow v7.27	2/5/2026	Course Completion
29	Priyanka G M	Cadence RTL to GDSII Flow v7.28	2/5/2026	Course Completion
30	Prajwal G K	Cadence RTL to GDSII Flow v7.29	2/5/2026	Course Completion
31	Nayana D Y	Cadence RTL to GDSII Flow v7.30	2/5/2026	Course Completion
32	Manjushree N H	Cadence RTL to GDSII Flow v7.31	2/5/2026	Course Completion
33	Manjunath U S	Cadence RTL to GDSII Flow v7.32	2/5/2026	Course Completion
34	Kushi G	Cadence RTL to GDSII Flow v7.33	2/5/2026	Course Completion
35	Komala Shifa R	Cadence RTL to GDSII Flow v7.34	2/5/2026	Course Completion
36	Jahnvi P	Cadence RTL to GDSII Flow v7.35	2/5/2026	Course Completion
37	Harshitha M S	Cadence RTL to GDSII Flow v7.36	2/5/2026	Course Completion
38	Gowtham A K	Cadence RTL to GDSII Flow v7.37	2/5/2026	Course Completion
39	Dilip Kumar S	Cadence RTL to GDSII Flow v7.38	2/5/2026	Course Completion
40	Deekshith B M	Cadence RTL to GDSII Flow v7.39	2/5/2026	Course Completion
41	Dhaneshwari H	Cadence RTL to GDSII Flow v7.40	2/5/2026	Course Completion
42	Abid Hussain	Cadence RTL to GDSII Flow v7.41	2/5/2026	Course Completion
43	Pragathi L R	Cadence RTL to GDSII Flow v7.42	2/5/2026	Course Completion
44	Bhoomika R	Cadence RTL to GDSII Flow v7.43	1/5/2026	Course Completion

STUDENT PLACEMENTS (JAN 2026 – JUNE 2026)

SL No.	Name of the Student & USN	Name of the Organization & Place
1	ADARSH DUBEY VS (4PM22EC002) PRAMOD G (4PM22EC074)	KIDVENTO EDUCATION
	DIVYASHREE G E (4PM22EC027)	HINDUSTAN AERONAUTICS
2	DARSHAN S H 4PM22EC023 MANASA S B 4PM22EC053 GANGA S B 4PM22EC032 NANDITHA GB 4PM22EC060 KEERTHANA H S 4PM22EC042 V PRANITHA 4PM22EC122 LAXMI S N 4PM22ECO48 LIKITHA L H 4PM22EC049	ADECCO INDIA
3	YASHAWINI B P (4PM22EC126)	DEDUCE TECHNOLOGIES
4	VAISHNAVI K N (4PM22EC123)	ELYTHRA EDUFYI TECH
5	SIRI RAMESH HITLALLI (4PM22EC104)	GRAYLINX

EVENTS CONDUCTED BY THE DEPARTMENT



Resource person addressing the students

The Department of Electronics and Communication Engineering successfully conducted an expert talk on “VLSI Designing and Testing” on 16th March 2026 for 6th semester students.



Felicitation during the valedictory function



Inauguration of the session by the chief guest



Resource person addressing the students

The Department of Electronics & Communication Engineering, in association with the Institution’s Innovation Council (IIC), PESITM, Shivamogga organised an expert talk on “AI & INNOVATION SPRINTS: RAPID PROTOTYPING FOR DIGITAL TRANSFORMATION” on 26th February 2026 for 4th semester students.

- The session was delivered by the esteemed resource person Dr.Naveen Kumar N M, Professor, Department of AIML, PESITM, Shivamogga.



The Department of Electronics & Communication Engineering, in association with the Institution's Innovation Council (IIC), PESITM, Shivamogga organised an expert talk on "Mastering the fundamentals of PCB Design" on 1st June 2026 for 4th semester students.

The objective of a PCB (Printed Circuit Board) design workshop is to equip participants with the practical skills and knowledge required to take an electronic circuit from an initial schematic diagram to a fully functional, manufactured, and tested physical circuit board.

ACTIVITIES/ACHIEVEMENTS BY FACULTIES

SL No.	Name of the Faculty	Details of Workshops / Conference / Training / Skill development / Seminar / Symposium / any other training attended (Name of the event, organizer, place, mode)	Date
1	Sangeetha B S, Kavya M P, Yashaswini N G, Neha S	AI and ML applications to Electrical and Electronics Engineering,JNNCE ,Shivamogga,Online	18/5/2026 22/5/2026 (5 days)
2	Dr. Suprith P G, Lavanya R, Dr. Mahanthesh P Mattada	28th ISTE State Level faculty Convention,JNNCE, Shimoga, Offline	23/05/2026

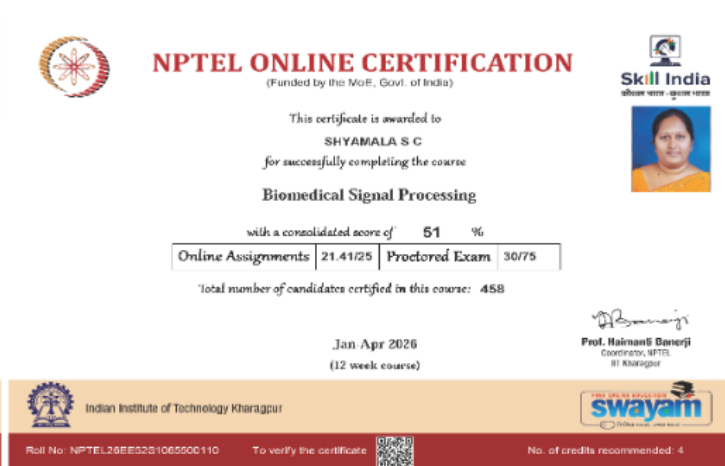
SL No.	Name of the Faculty	Publication Details (Title, Publisher, Volume, Page No.)	Date	Index / Impact Factor
1	Dr. Narayanswamy G	Q-MOHO: Q-Machine Learning Guided Multi Objective power optimization for RIS-assisted MIMO-NOMA systems, International Journal of Electronics and Telecommunication, Vol 72, no 2, pp 1-8	16/05/2026	0.5
2	Dr. Suprith P G			

SL No.	Name of the Participant	Conference paper Details	Date
1	Dr. Mahantesh P Mattada	Conference Proceedings as Book Chapter, "Quantum Leap in Healthcare Security: The Role of Quantum Watermarking." International Conference on Power Engineering and Intelligent Systems (PEIS). Singapore: Springer Nature Singapore, 2025.	Jan, 2026

FACULTIES ACTED AS A REVIEWERS



NPTEL CERTIFICATIONS BY FACULTIES



Patron

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Principal, PESITM

Editorial Team

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Ms. Triveni (4th Sem, ECE)
Ms. Rakshitha M K (6thSem, ECE)
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Thank You

